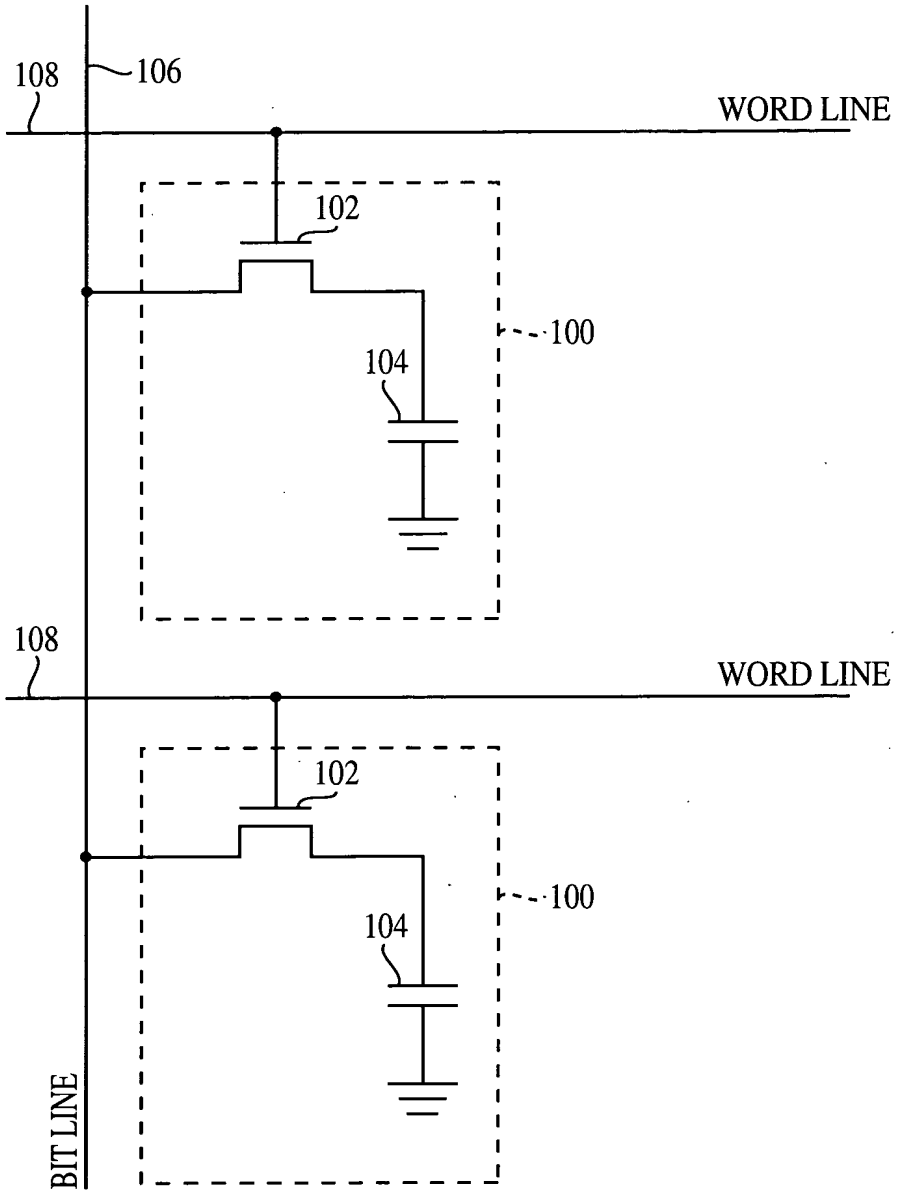

FIG. 1
(PRIOR ART)

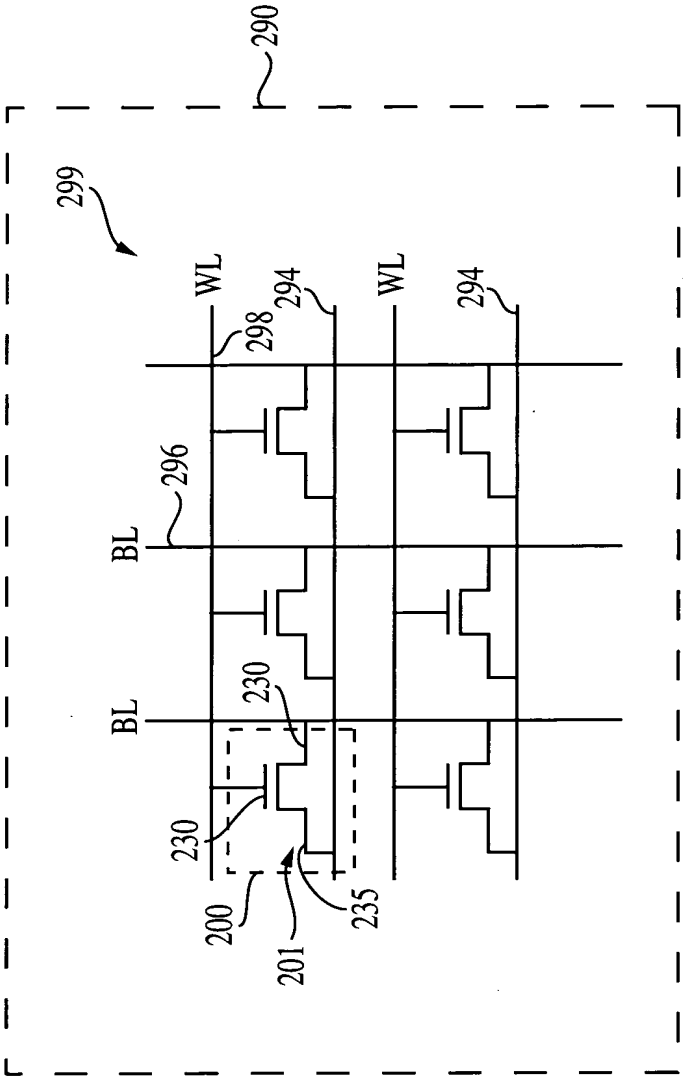

FIG. 2

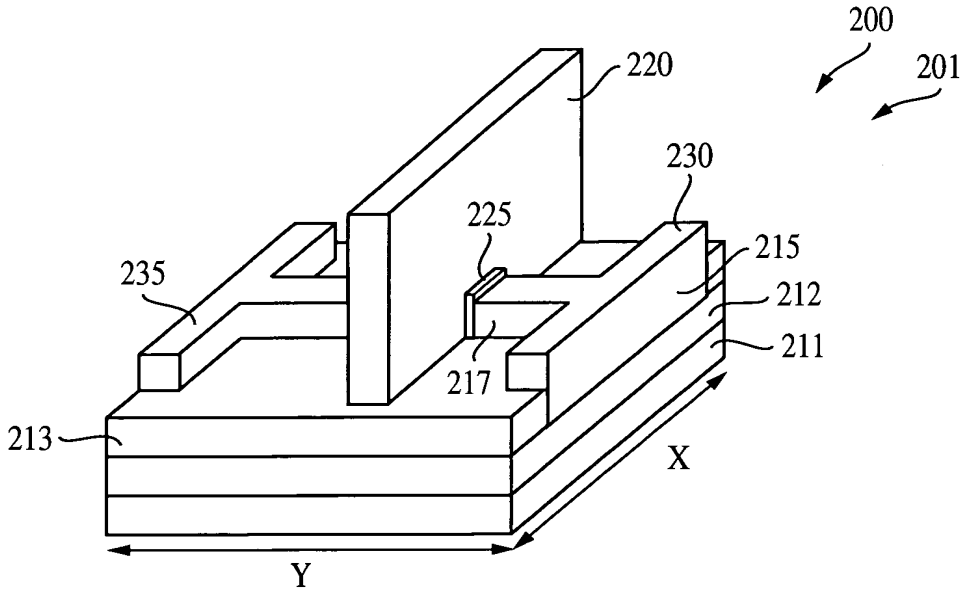
200
201
220
230
225
215
235
212
211
217
213
X
Y

FIG. 3

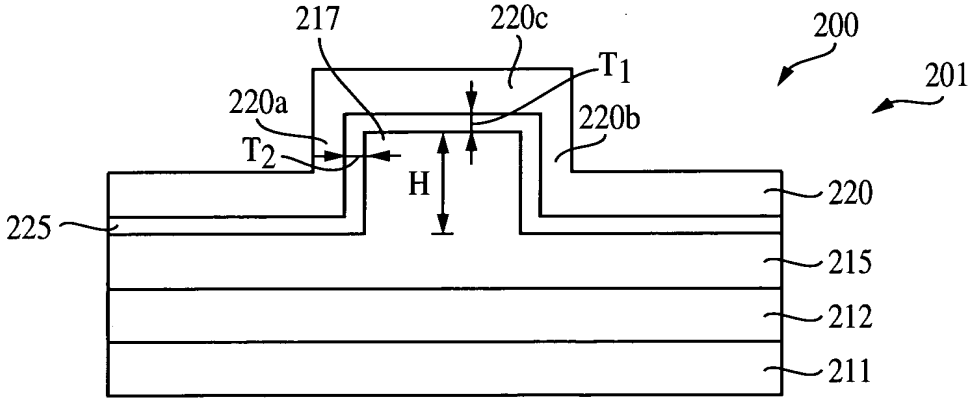
217
220c
200
T1
201
220a
220b
T2
H
220
225
215
212
211

FIG. 4

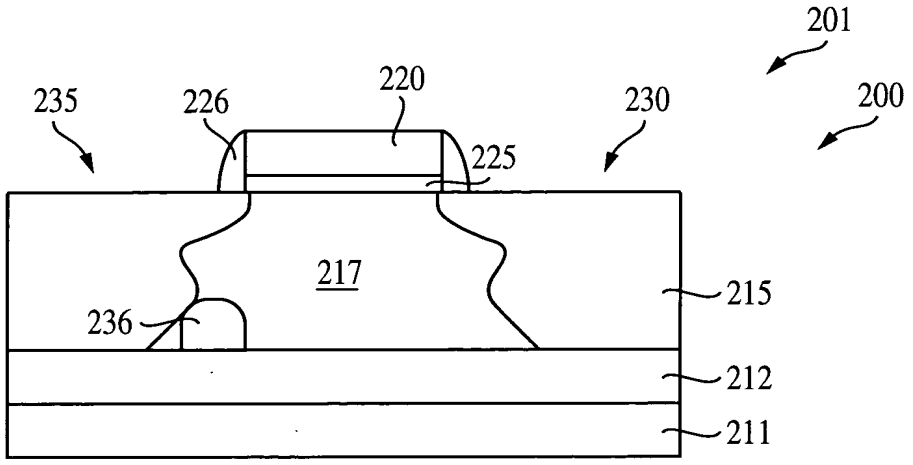

FIG. 5

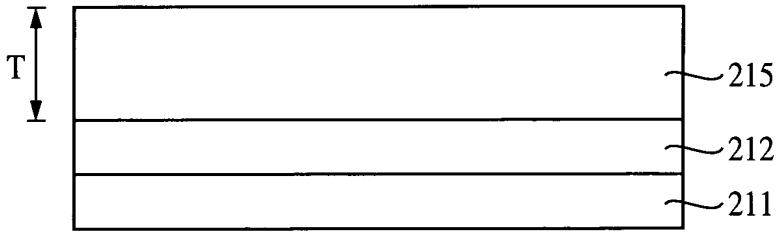

FIG. 6A

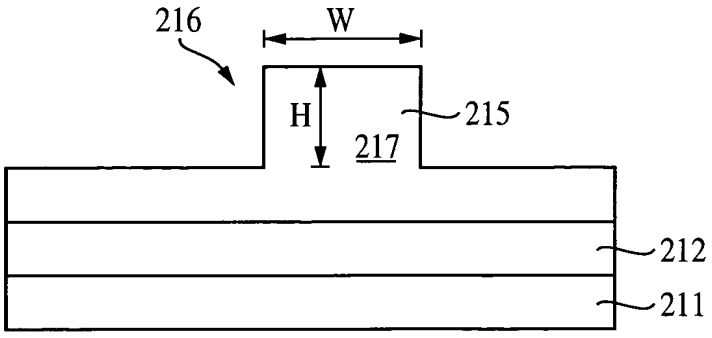

FIG. 6B

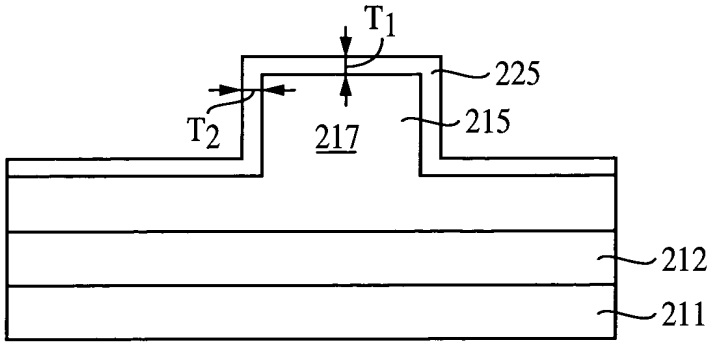

FIG. 6C

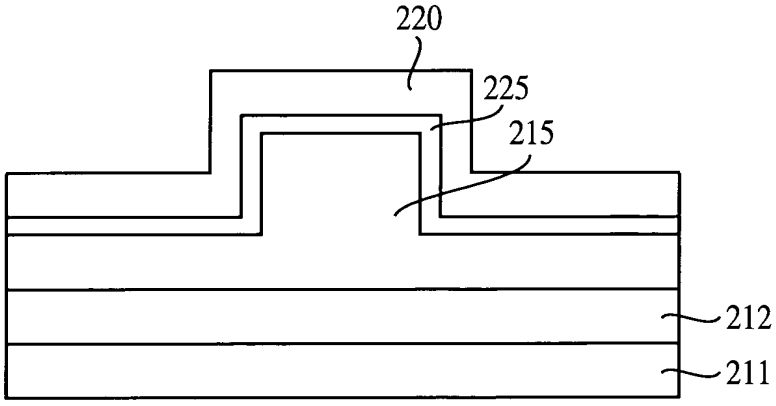

FIG. 6D

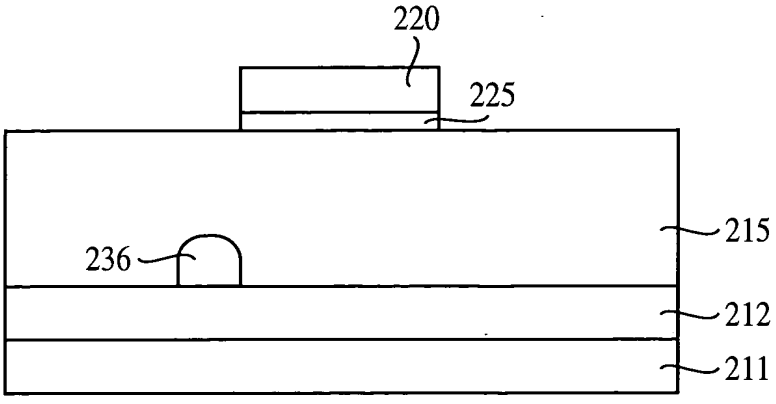

FIG. 6E

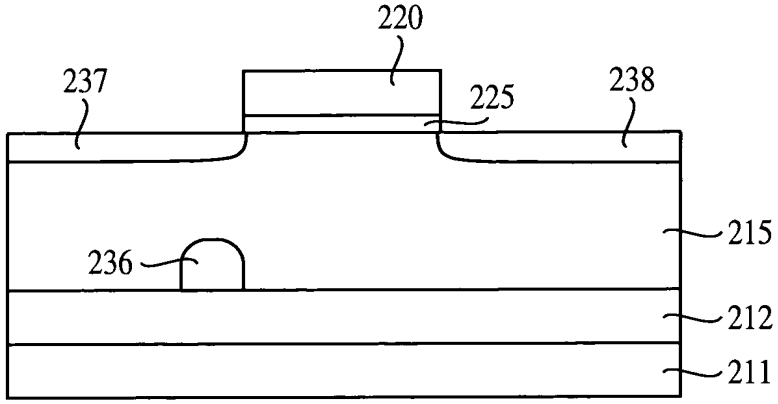

FIG. 6F

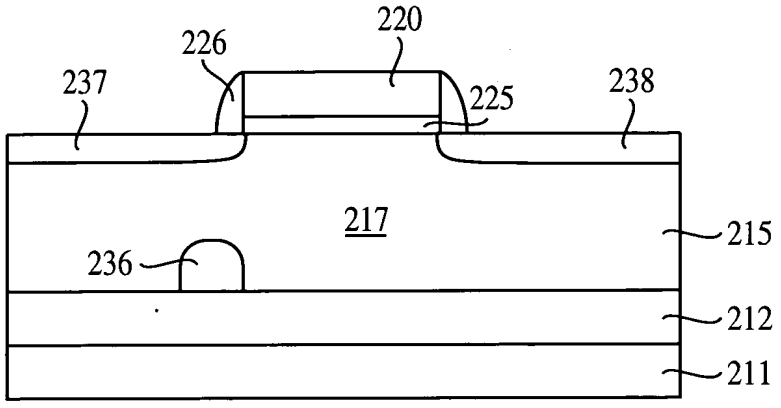

FIG. 6G

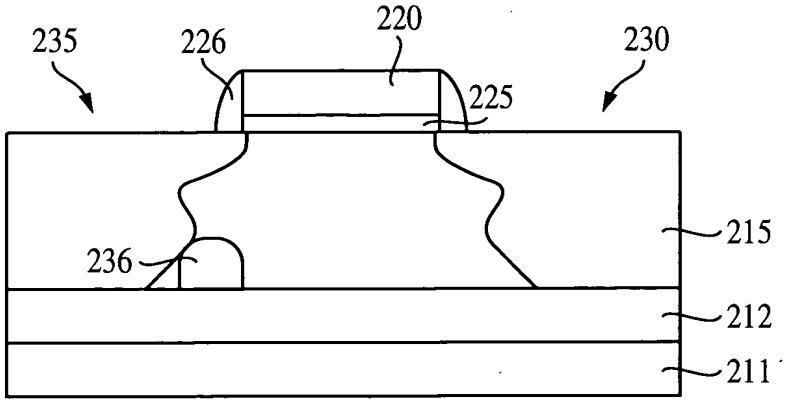

FIG. 6H

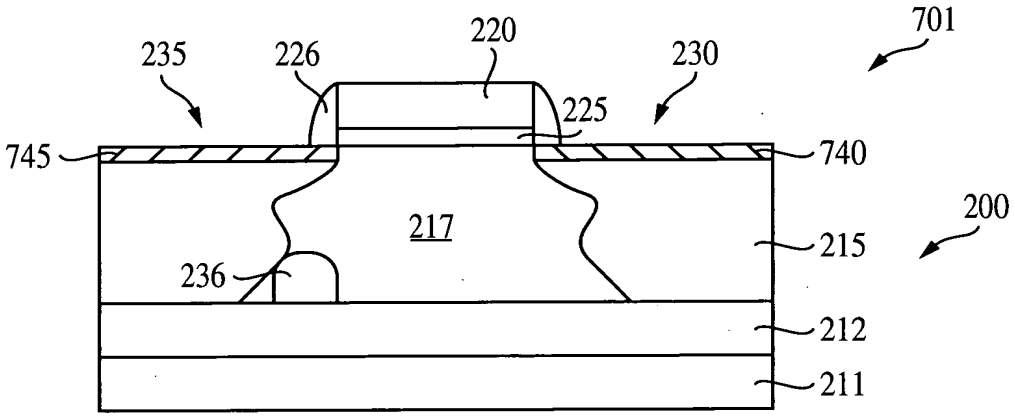
701
235
226
220
230
225
745
740
217
200
215
236
212
211

FIG. 7

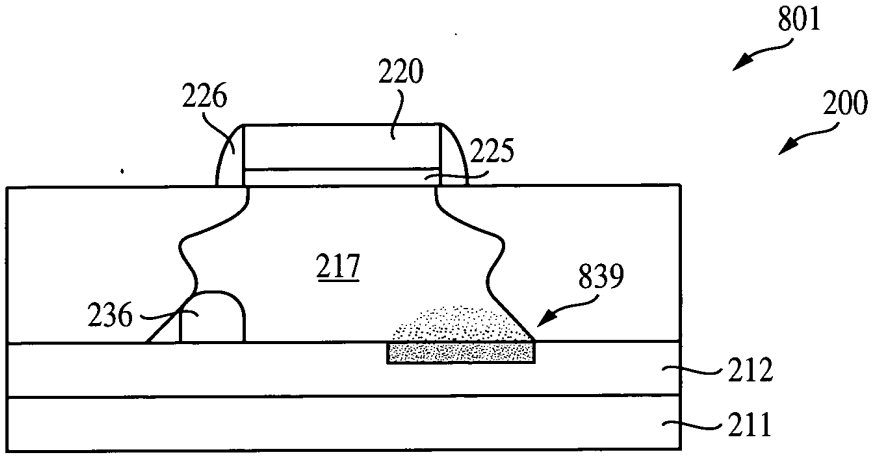
801
200
226
220
225
217
839
236
212
211

FIG. 8

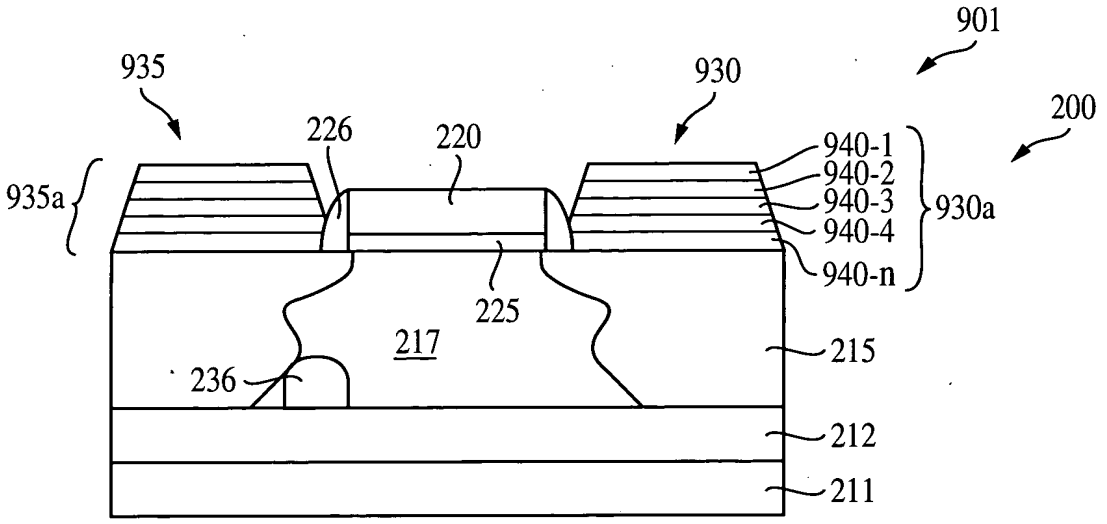
901
935
930
200
226
220
940-1
940-2
940-3
940-4
940-n
935a
930a
225
217
215
236
212
211

FIG. 9A

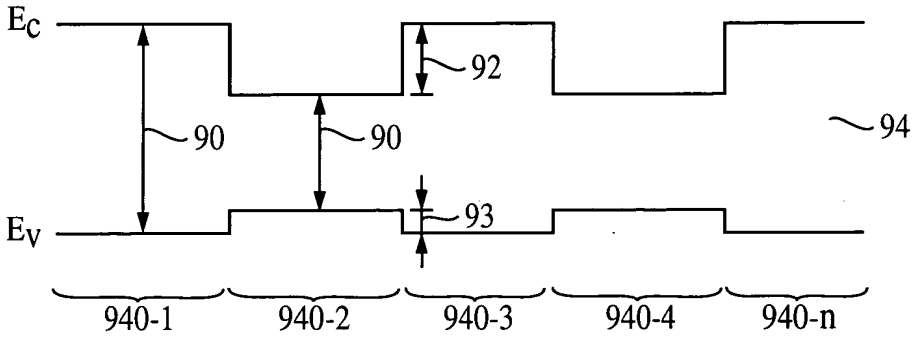
E_C
92
90
90
94
93
E_V
940-1
940-2
940-3
940-4
940-n

FIG. 9B

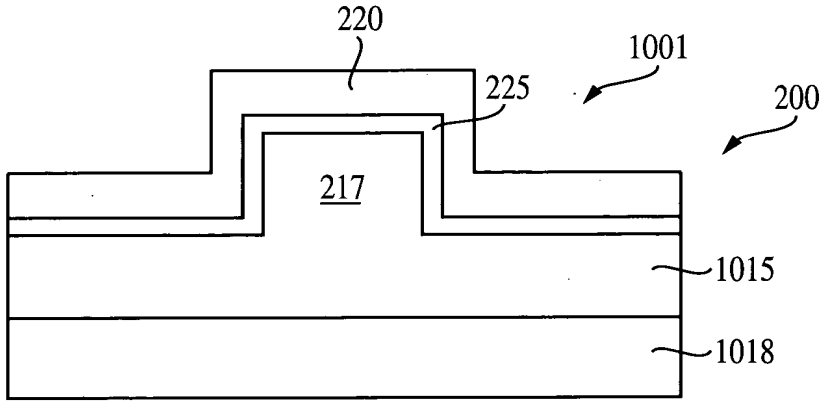

FIG. 10

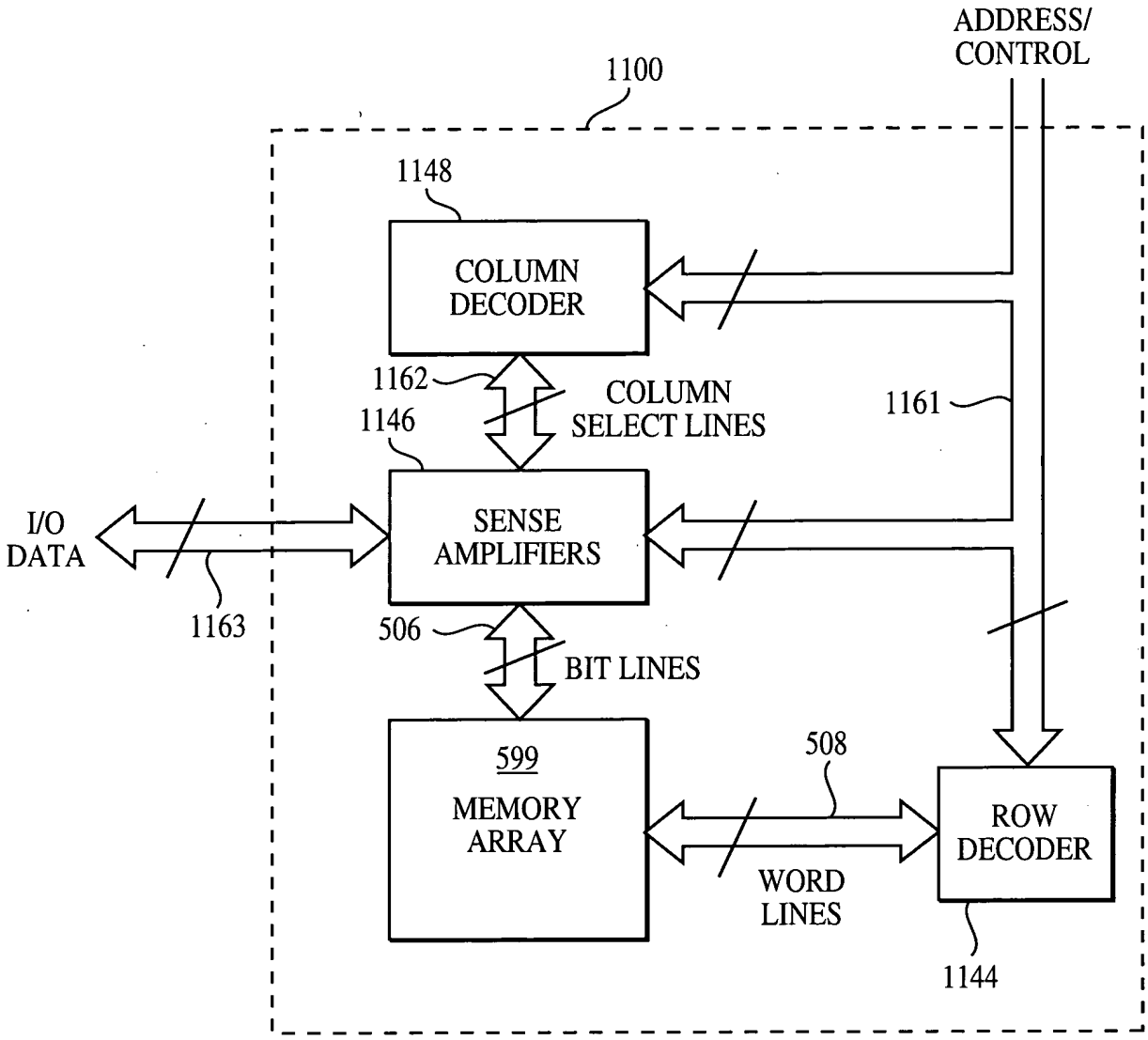
ADDRESS/
CONTROL
1100
1148
COLUMN
DECODER
1162
1146
COLUMN
SELECT LINES
1161
I/O
DATA
SENSE
AMPLIFIERS
1163
506
BIT LINES
599
MEMORY
ARRAY
508
ROW
DECODER
WORD
LINES
1144

FIG. 11

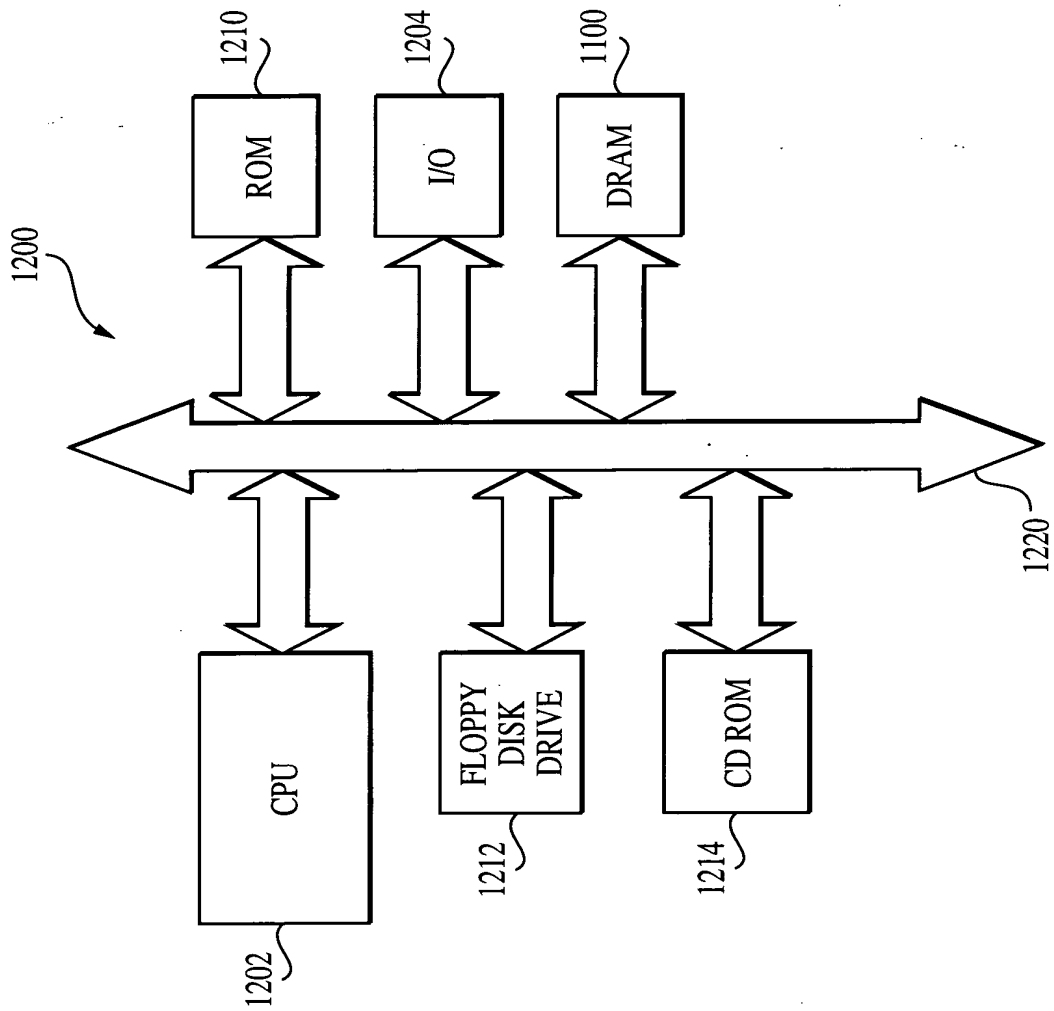
1200
1210
ROM
1204
I/O
1100
DRAM
1220
CPU
1202
FLOPPY DISK DRIVE
1212
CD ROM
1214

FIG. 12